

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S7	25444933	@ad<"20040408"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 09:05
S8	56	S7 And ((PLD ((Programmable logic field) adj2 (array logic device field)) CPLD PAL PLA FPLA EPLD EEPLD LCA FPGA)) And ((Partition\$4 Split\$4 divi\$5 decompos\$3 allot) with (PLD ((Programmable logic field) adj2 (array logic device field)) CPLD PAL PLA FPLA EPLD EEPLD LCA FPGA) with (block module cell group section)) And (RTL (Register with transfer with (language logic))) And ((Verif\$5 check\$4 simulat\$4 synthes\$6 violat\$5 satisf\$6 debug\$4) with (RTL (Register with transfer with (language logic))))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 12:08
S9	22	S8 and ((RTL (Register with transfer with (language logic))) with model)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 09:12
S10	36	S7 and (block adj bas\$4 adj design\$4 and rtl and (simulat\$4 debug\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/01 12:11